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The Intel 8086 high performance 16-bit CPU is available in three clock rates: 5, 8 and 10 MHz. The CPU is implemented in N-Channel, depletion load, silicon gate technology (HMOS-III), and packaged in a 40-pin CERDIP or plastic package. The 8086 operates in both single processor and multiple processor configurations to achieve high performance levels.

Intel 8086, 8086-1, 8086-2 User Manual

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The 8086 and 8088 Central Processing Units Processor Overview
Processor Architecture - Execution Unit - Bus Interface Unit -
General Registers - Segment Register - Instruction Pointer - Flags
- 8080 /8085 Register and Flag Correspondance ... Intel MCS86
Manual Author:

Intel MCS86 Manual - Ceibo

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Intel® 64 and IA-32 Architectures Software Developer's Manual

This manual is directed to the programmer who is developing programs for the 8086 with PL/M-86 or ASM86 or any other language translator that generates object code compatible with the tools described in this manual.

8086 FAMILY UTILITIES USER'S GUIDE - NJ7P

The 8086 (also called iAPX 86) is a 16-bit microprocessor chip designed by Intel between early 1976 and June 8, 1978, when it was released. The Intel 8088 , released July 1, 1979, [4] is a slightly modified chip with an external 8-bit data bus (allowing the use of cheaper and fewer supporting ICs), [note 1] and is notable as the processor used in the original IBM PC design.

Intel 8086 - Wikipedia

OPERATING SYSTEM PROCESSOR, 8086 datasheet, 8086 circuit,

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8086 Datasheet(PDF) - Intel Corporation

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February 10, 2003 Intel 8086 architecture 6 8086 instruction set architecture The 8086 is a two-address, register-to-memory architecture. This is interpreted as $a = a + b$. — a can be a register or a memory address. — b can be a register, a memory reference, or a constant. —But a and b cannot both be memory addresses.

Intel 8086 architecture - howard huang

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